2.4.2 Number of Full-Time Teachers with Ph.D							
	16						

Name of full-time teachers with PhD / D.M. / M.Ch. / D.N.B Super Specialty /DSc / DLitt during the year	Qualification (PhD / D.M. / M.Ch. D.N.B Super Specialty /DSc / DLitt) and Year of obtaining	Whether recognised as a research guide for PhD?	_	Is the teacher still serving the institution?/If not,when did he/she leave the institution?	Name of the scholar	Month and Year of registration of the scholar	PhD Scholar's Title of the Thesis
Dr. K.L.PALANISAMY	Ph.D & 2015	yes	2016	YES	NIL	NIL	NIL
Dr. K.VIGNESH	Ph.D & 2017	No	NIL	YES	NIL	NIL	NIL
Dr.M.SEENIRAJAN	Ph.D.	NO	NIL	YES	NIL	NIL	NIL
Dr.R.VIJAYARENGAN	Ph.D & 2017	No	NIL	YES	NIL	NIL	NIL
Dr.B.SUJATHA	Ph.D & 2014	Yes	NIL	YES	NIL	NIL	NIL
Dr.M.SAKTHIVEL	Ph.D & 2017	No	NIL	YES	NIL	NIL	NIL
Dr.G.JAYAMURUGAN	Ph.D & 2019	No	NIL	YES	NIL	NIL	NIL
Dr.S.RADHA	Ph.D & 2020	No	NIL	YES	NIL	NIL	NIL
Dr.C.VENKATESH	Ph.D & 2007	Yes	2016	YES	Mr.P.Sivasasankaran, Mr.M.Arunkumar V.T.Inbavalli S.Mahendran	January 2018	Smart Mind Reading through Diverse Intelligenge technique Application IOT in Automobiles
Dr.C.AARTHI	Ph.D & 2019	No	NIL	YES	NIL	NIL	NIL
Dr.P.RAMESHKUMAR	Ph.D & 2014	No	NIL	YES	NIL	NIL	NIL
					A.Vijay	JULY'17	Developing Optimal Spectrum Sharing Protocol And Optimal Linear Precoding For Multi- Carrier Code-Division Multiple Access Using Massive Multiple Input Multiple Output In 5G Wireless Networks
					A.Yogeshwaran	JANUARY'18	Efficient Wideband Low Noise Amplifier (WLNA) Based Industrial Microstrip Antenna Design Using Optimization Techniques

Dr.K.Umadevi	PhD	Yes	2016	YES	V.Vijayakumar	JANUARY'18	Real Time Personalized Target Analysis With Multi Level Access Restriction For Improve Privacy Preservation In Cloud Environment
					S.S.Sathya	JANUARY'18	Dynamic Rate Aware IE Strategic Classified Key Distribution Based Secure Routing For QOS Development in WMN
					N.Venkateswaran	JANUARY'18	Pattern Matching inside the context of network intrusion detection systems in mobile AdHoC network
Dr.M.Selvakumar	Ph.D & 2017	NO	NIL	YES	NIL	NIL	NIL
Dr.S.Thangaprakash	Ph.D & 2011	NO	NIL	YES	NIL	NIL	NIL
Dr.P.Ponmurugan	Ph.D & 2018	Yes	NIL	YES	NIL	NIL	NIL
Dr. P.Govindasamy	Ph.D & 2014	NO	NIL	YES	NIL	NIL	NIL



CENTRE FOR RESEARCH

ANNA UNIVERSITY CHENNAI - 600 025

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: +91-44-2220 1213

: dirresearch@annauniv.edu dirresearch@gmail.com



Date: 13.05.2016

Lr. No: SUPR/27th/DB

To
Dr. Venkatesh C
Professor
Department of Electronics and Communication Engineering
Sengunthar Engineering College,
Tiruchengode - 637205

Sir/Madam.

Sub : Ph.D./M.S. (By Research) Programme - Supervisor Recognition - Orders - Issued.

Ref: Your application for Supervisor Recognition.

I am by direction to inform that you are recognized as a Supervisor (Ref.No: 2740091) for guiding Ph.D. and M.S. (By Research) scholars of this University under the faculty of Information and Communication Engineering.

Area of Specialization: "Communication Systems, Wireless Sensor Networks, Soft Computing"

The functions and responsibilities of the supervisor are described under clause (7) of the Ph.D./M.S. (By Research) regulations of this University and applicable as and when amendments are made to the Regulations from time to time.

Supervisors working in the recognized R&D Centre of Industries/non-recognized research Departments of Engineering College/Architecture Schools situated within Tamil Nadu shall function only as Joint Supervisor.

I request you to adhere strictly the regulations (available in https://cfr.annauniv.edu/research/instructions.php) when you start guiding scholars.

In all future correspondence quote 2740091 for reference.

DIRECTOR

Copy to:

The Head
Department of Electronics and Communication Engineering
Sengunthar Engineering College,
Tiruchengode - 637205



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dirresearch@annauniv.edu dirresearch@gmail.com

Date: 31.12.2016

Lr. No: SUPR/27th/DB

To
Dr. Uma Devi K
Professor
Department of Electrical and Electronics Engineering
Sengunthar Engineering College,
Tiruchengode-637205

Sir/Madam,

Sub: Ph.D./M.S. (By Research) Programme - Supervisor Recognition - Orders - Issued.

Ref: Your application for Supervisor Recognition.

I am by direction to inform that you are recognized as a Supervisor (Ref.No: 2830071) for guiding Ph.D. and M.S. (By Research) scholars of this University under the faculty of Electrical Engineering.

Area of Specialization: "Brush less DC Machines, Finite Element Analysis, Exterior Rotor, Permanent Magnet"

The functions and responsibilities of the supervisor are described under clause (7) of the Ph.D./M.S. (By Research) regulations of this University and applicable as and when amendments are made to the Regulations from time to time.

Supervisors working in the recognized R&D Centre of Industries/non-recognized research Departments of Engineering College/Architecture Schools situated within Tamil Nadu shall function only as Joint Supervisor.

I request you to adhere strictly the regulations (available in https://cfr.annauniv.edu/research/instructions.php) when you start guiding scholars.

In all future correspondence quote 2830071 for reference.

DIRECTOR

Copy to:

The Head
Department of Electrical and Electronics Engineering
Sengunthar Engineering College,
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CENTRE FOR RESEARCH

1 9 MAY 2016

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dirresearch@annauniv.edu dirresearch@gmail.com

Date: 13.05.2016

The bushards

Lr. No: SUPR/27th/DB

Dr. Sujatha B Professor

Department of Computer Science and Engineering

Sengunthar Engineering College,

Tiruchengode - 637205

Sir/Madam.

Sub: Ph.D./M.S. (By Research) Programme - Supervisor Recognition - Orders - Issued.

Ref: Your application for Supervisor Recognition.

I am by direction to inform that you are recognized as a Supervisor (Ref.No: 2740126) for guiding Ph.D. and M.S. (By Research) scholars of this University under the faculty of Information and Communication Engineering.

Area of Specialization: "Data Mining, Cloud Computing, Networks, Image Processing"

The functions and responsibilities of the supervisor are described under clause (7) of the Ph.D./M.S. (By Research) regulations of this University and applicable as and when amendments are made to the Regulations from time to time.

Supervisors working in the recognized R&D Centre of Industries/non-recognized research Departments of Engineering College/Architecture Schools situated within Tamil Nadu shall function only as Joint Supervisor.

regulations (available strictly the in adhere request VOU to https://cfr.annauniv.edu/research/instructions.php) when you start guiding scholars.

In all future correspondence quote 2740126 for reference.

Copy to:

The Head Department of Computer Science and Engineering Sengunthar Engineering College, Tiruchengode - 637205

Lr. No: SUPR/32nd/DB

John 14

Date: 24.05.2019

To

Dr. Ponmurugan P
Assistant Professor
Electrical and Electronics Engineering
Sri Krishna College of Technology
Coimbatore-641042

Sir/Madam,

Sub: Ph.D.Programme - Supervisor Recognition - Orders - Issued, Ref: Your application for Supervisor Recognition.

I am by direction to inform that you are recognized as a Supervisor (Ref.No: 3330034) for guiding Ph.D. scholars of this University under the faculty of Electrical Engineering.

Area of Specialization: "Machine Design, Soft Computing, Control System"

The functions and responsibilities of the supervisor are described under clause (7) of the Ph.D.regulations of this University and applicable as and when amendments are made to the Regulations from time to time.

Supervisors working in the recognized R&D Centre of Industries/non-recognized research Departments of Engineering College/Architecture Schools situated within Tamil Nadu shall function only as Joint Supervisor.

I request you to adhere strictly the regulations (available in https://cfr.annauniv.edu/research/instructions.php) when you start guiding scholars.

In all future correspondence quote 3330034 for reference.

DIRECTOR

Copy to:

The Head Electrical and Electronics Engineering Sri Krishna College of Technology Coimbatore-641042

K. E. Har